A picture containing person, clothing, indoor

Description automatically generatedLogo

Description automatically generatedA screenshot of a computer

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**Lynn Conway**

Education: Studied at MIT and Columbia University.

B.S and M.S. in Electrical Engineering from Columbia University

Occupation: Worked at IBM Research, Computer Architect at Memorex, Assistant Director for Strategic computing at DARPA  
Professor of Electrical Engineering and Computer Science and Associate Dean of Engineering at the University of Michigan,   
Worked at Xerox PARC

Contributions and Awards: Lynn Conway is a pioneer of microelectronic chip design because of her work at the Palo Alto Research Center. She invented scalable design rules for VLSI chip design. wrote introduction to VLSI systems, which influenced modern silicon chip design. Prior to this she did research at IBM and invented multiple-out-of-order dynamic instruction scheduling for supercomputers. However, she was later fired from IBM because she was undergoing gender transition.

She has been awarded the Pender Award of the Moore School of the University of Pennsylvania, Wetherill Medal of the Franklin Institute, Secretary of Defense Meritorious Achievement Award, and Society of Women Engineers National Achievement Award

January 2, 1938 - Present

Birthplace:

Mt. Vernon, New York, USA

Country of Origin: USA